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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of:

**Shigeru ATSUMI**

Serial No.: 09/028,276

Filed: February 24, 1998

For: Semiconductor Integrated Circuit Device and  
Flash EEPROM

Atty. Docket No.: 001701.73982

Group Art Unit: 2815

Examiner: Eckert II, G.

Confirmation No.: 4461

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(d)**

U.S. Patent and Trademark Office  
Customer Service Window, Mail Stop AF  
Randolph Building  
401 Dulany Street  
Alexandria, VA 22314

Sir:

In accordance with Applicants' duty of disclosure, the following information is submitted for consideration by the United States Patent and Trademark Office in connection with the above-captioned application. The information is identified on the attached PTO 1449 form.

In accordance with the rules of practice under 37 C.F.R. § 1.98, including the waiver of October 19, 2004, Applicants are only providing photocopies of non-US patent and literature documents.

The undersigned certifies under 37 C.F.R. § 1.97(e)(1) that each reference cited herein was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

Also enclosed is a copy of the office action, including English translation, for a corresponding foreign application indicating the relevance of the references cited therein.

The Commissioner is authorized to charge the requisite fee under 37 C.F.R. § 1.17(p) (\$180.00) to Deposit Account No. 19-0733. Further, the Commissioner is authorized to charge any additional fees which may be required or credit any overpayment to our Deposit Account No. 19-0733.

06/03/2005 SZEWDIE1 00000134 190733 09028276

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Applicants do not waive any right to take appropriate action to establish patentability over the listed documents should they be applied as references against the claims of the present application.

It is respectfully requested that the Examiner fully consider each of the documents, initial the enclosed Form PTO-1449 in the appropriate place to indicate that the document has been considered, and return a copy of the initialed form to the undersigned in accordance with MPEP Section 609.

Respectfully submitted,



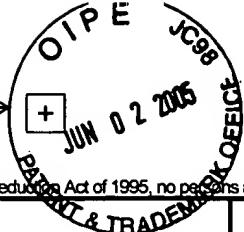
Gary D. Fedorochko  
Reg. No. 35,509

Date: June 2, 2005

Banner & Witcoff, Ltd.  
1001 G Street, N.W.  
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(202) 508-9100

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**Substitute for form 1449A/PTO**

# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 1 of 1

Complete if Known	
Application Number	09/028,276
Filing Date	February 24, 1998
First Named Inventor	Shigeru ATSUMI
Group Art Unit	2815
Examiner Name	Eckert II, G.
Attorney Docket Number	001701.73982

## **U.S. PATENT DOCUMENTS**

## FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sub>6</sub>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
		JP	06-188387	A	Mitsubishi Electric	7/8/1994		Abstract
		JP	05-006665	A	Hitachi	1/14/1993		Abstract
		JP	08-204142	A	OKI Electric	8/9/1996		Abstract
		JP	05-259289	A	Fujitsu	10/8/1993		Abstract
		JP	07-169293	A	Mitsubishi Electric	7/4/1995		Abstract

## **OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>

Examiner Signature		Date Considered	
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**EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.